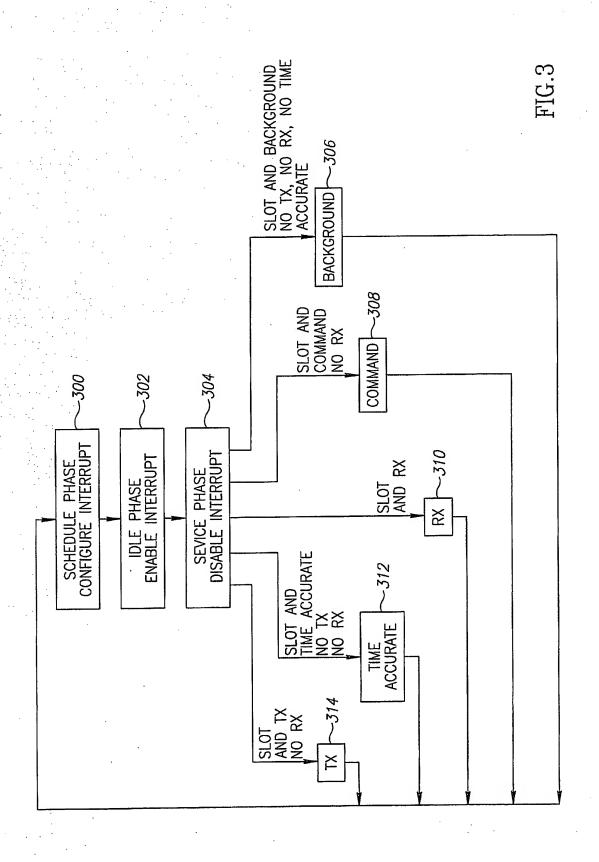
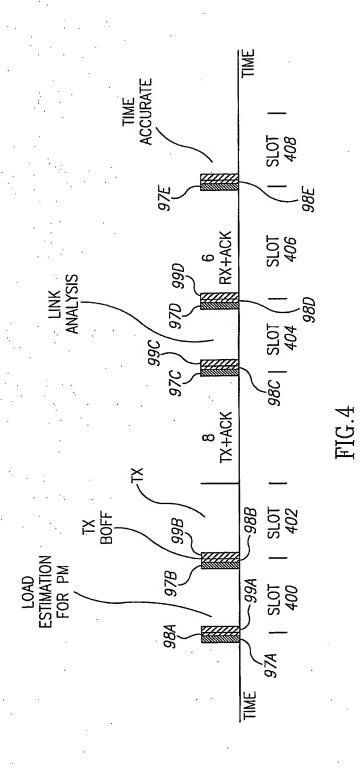


FIG.2





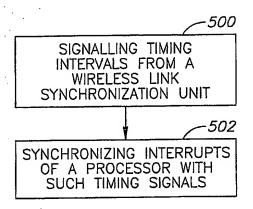


FIG.5

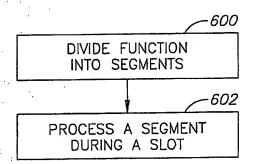


FIG.6

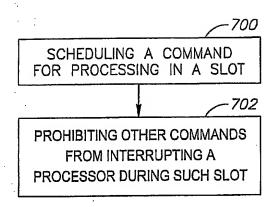


FIG.7

-800

DELAYING AN INTERRUPT OF A PROCESSOR UNTIL THE START OF A NEXT SLOT

FIG.8